

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (currently amended) A video display ~~er facilitating re-setting that facilitates resetting~~ of channel and external input settings, the video display comprising:
 - a tuner configured to select a broadcast signal;
 - an external signal input unit configured to receive an external signal;
 - a signal processing unit configured to process one of the broadcast signal selected and the external signal, and to reproduce video images on a display and audio output through a speaker;
 - and
 - a control unit configured to [[store]] sequentially store setting data for setting environments of said tuner, said external signal input unit, and said signal processing unit according to an externally applied control signal, and to set the setting environment of at least one of said tuner, said external signal input unit, and said signal processing unit with one of previous setting data and subsequent setting data based on any one data set of the sequentially stored setting data in response to a state changing signal applied from an external source.

2. (currently amended) The video display as claimed in claim 1, said signal processing unit comprising:

a signal separator configured to select separate one of the broadcast signal ~~from said tuner selected~~ and the external signal ~~from said external signal input unit~~ into one of a video signal and an audio signal;

a video signal processing unit configured to process and [[to]] output to the display the video signal of said signal separator; and

an audio signal processing unit configured to process and [[to]] output to the speaker the audio signal of said signal separator.

3. (currently amended) The video displayer as claimed in claim 1, said control unit comprising:

a micro controller configured to receive and [[to]] interpret the control signal and the state changing signal ~~received from an external source~~, and to control at least one of said tuner, said external signal input unit, and said signal processing unit; and

a state setting unit configured to store setting data output from said micro controller, and to output, in response to the state changing signal, one of previous setting data of the stored setting data and subsequent setting data of the stored setting data to at least one of said tuner, said external signal input unit, and said signal processing unit.

4. (currently amended) The video displayer as claimed in claim 3, said state setting unit comprising:

an address generator configured to generate an address in response to the state changing signal;

a parallel shift register configured to output stored setting data to at least one of said tuner, said external signal input unit, and said signal processing unit, based on the address provided generated by said address generator; and

a register output detector configured to decrease the address generated by said address generator by one address block[[],] upon detecting a shift operation of the parallel shift register.

5. (currently amended) The video displayer as claimed in claim 4, said state changing signal comprising:

a first state changing signal configured to re-set reset said video displayer according to previous setting data of the stored setting data; and

a second state changing signal configured to re-set reset said video displayer according to subsequent setting data of the stored setting data.

6. (currently amended) The video displayer as claimed in claim 5, [[said]] wherein the parallel shift register [[being]] is a First In First Out (FIFO) parallel shift register configured to output setting data corresponding to [[the]] a respective address block.

7. (currently amended) The video displayer as claimed in claim 6, said address generator comprising:

an address register configured to store an address of setting data most recently stored to said parallel shift register; and

an adder/subtractor configured to one of increase and decrease an address stored in said address register in response to the state changing signal ~~outputted from said micro controller~~.

8. (currently amended) The video displayer as claimed in claim 7, wherein said register output detector is configured to detect whether setting data is discarded resulting from a shift operation of said FIFO parallel shift register and, upon detecting discarding of the setting data, to decrease [[an]] the address output from generated by said address generator by one address block.

9. (original) A video displayer having a tuner configured to select a broadcast signal; an external signal input unit configured to receive an external signal; a signal processing unit configured to process one of the broadcast signal selected and the external signal and to reproduce a video signal on a display and an audio signal to a speaker; and a control unit configured to store sequentially setting data for setting environments of said tuner, said external signal input unit, and said signal processing unit, and to set, according to an externally applied state changing signal, a setting environment of at least one of said tuner, said external signal input unit, and said signal processing unit with one of previous setting data and subsequent

setting data, based on any one of the sequentially stored setting data, the video displayer comprising:

a control key configured to control said video displayer; and

at least two state changing keys configured to generate the state changing signal.

10. (currently amended) The ~~remote control unit~~ video displayer as claimed in claim 9, said state changing keys comprising:

a previous state selection key configured to select a previous setting state based on the setting data stored in said video displayer; and

a subsequent state selection key configured to select a subsequent setting state based on the setting data stored in said video displayer.

11. (currently amended) A method of re-setting resetting setting information for broadcast channels and external data inputs in a video displayer, comprising:

sequentially storing the broadcast channels of the video displayer and setting data for images and sounds for the broadcast channels or the external inputs, whenever one of the broadcast channels or the external inputs is switched;

re-setting resetting the video displayer according to one of previous setting data and subsequent setting data, based on setting data for a broadcast channel to which the video

displayer is tuned as reference setting data in response to a state changing signal from an external source; and

~~re-setting~~ resetting the ~~re-set~~ reset setting data as reference setting data.

12. (currently amended) The method as claimed in claim 11, wherein in the sequentially storing, ~~operation~~ the ~~order of plural~~ setting data [[are]] is stored in a First In First Out (FIFO) parallel shift register configured to discard the setting data, in [[the]] an order of input, when the sequentially stored setting data exceeds a capacity of the FIFO parallel shift register.

13. (currently amended) The method as claimed in claim 11, the state changing signal comprising:

a first state changing signal configured to ~~re-set~~ reset the video display based on previous setting data with respect to the reference setting data; and

a second state changing signal configured to ~~re-set~~ reset the video display based on subsequent setting data with respect to the reference setting data.

14. (new) The video display of claim 1, wherein the state changing signal comprises:
a first state changing signal configured to reset the video display according to previous setting data of the stored setting data; and

a second state changing signal configured to reset the video display according to subsequent setting data of the stored setting data.

15. (new) The video display of claim 4, wherein the parallel shift register is a First In First Out (FIFO) parallel shift register configured to output setting data corresponding to a respective address block.

16. (new) The video display of claim 15, wherein the register output detector is configured to detect whether setting data is discarded resulting from a shift operation of the FIFO parallel shift register and, upon detecting discarding of the setting data, to decrease the address generated by the address generator by one address block.

17. (new) The video display of claim 4, wherein the address generator comprises:
an address register configured to store an address of setting data most recently stored to the parallel shift register; and
an adder/subtractor configured to one of increase and decrease an address stored in the address register in response to the state changing signal.

18. (new) A method of resetting setting information for broadcast channels and external data inputs in a video display, comprising:

sequentially storing, in a register of the video display, the broadcast channels of the video display and setting data for images and sounds for the broadcast channels or the external inputs, whenever one of the broadcast channels or the external inputs is switched;

resetting the video display according to one of previous setting data and subsequent setting data, based on setting data for a broadcast channel to which the video display is tuned as reference setting data in response to a state changing signal from an external source; and

resetting the reset setting data as reference setting data.

19. (new) The method of claim 18, wherein in the sequentially storing, the setting data is stored in a First In First Out (FIFO) parallel shift register configured to discard the setting data, in an order of input, when the setting data exceeds a capacity of the FIFO parallel shift register.

20. (new) The method of claim 18, wherein the state changing signal comprises:
a first state changing signal configured to reset the video display based on previous setting data with respect to the reference setting data; and
a second state changing signal configured to reset the video display based on subsequent setting data with respect to the reference setting data.